

FIG. 1

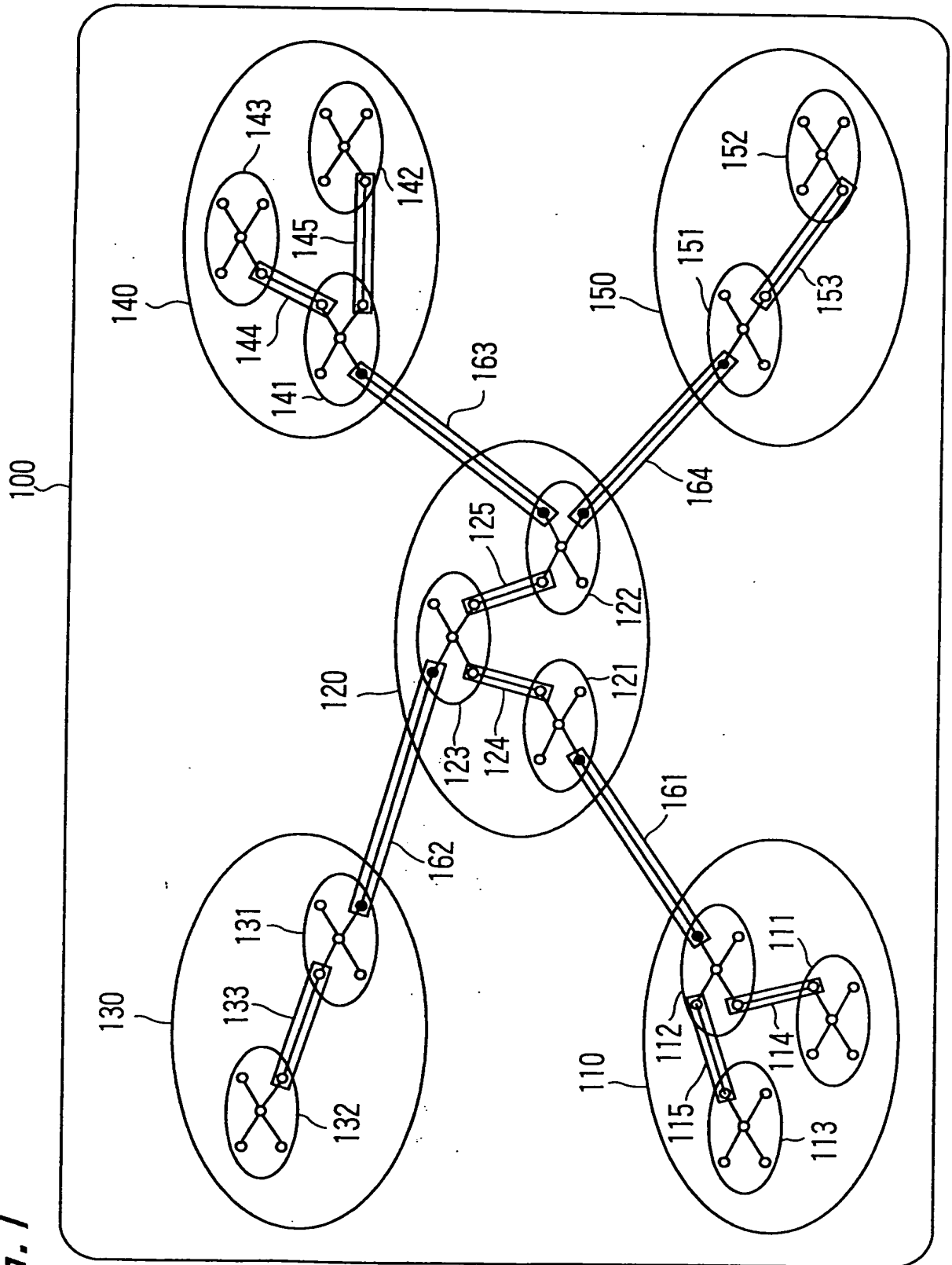


FIG. 2

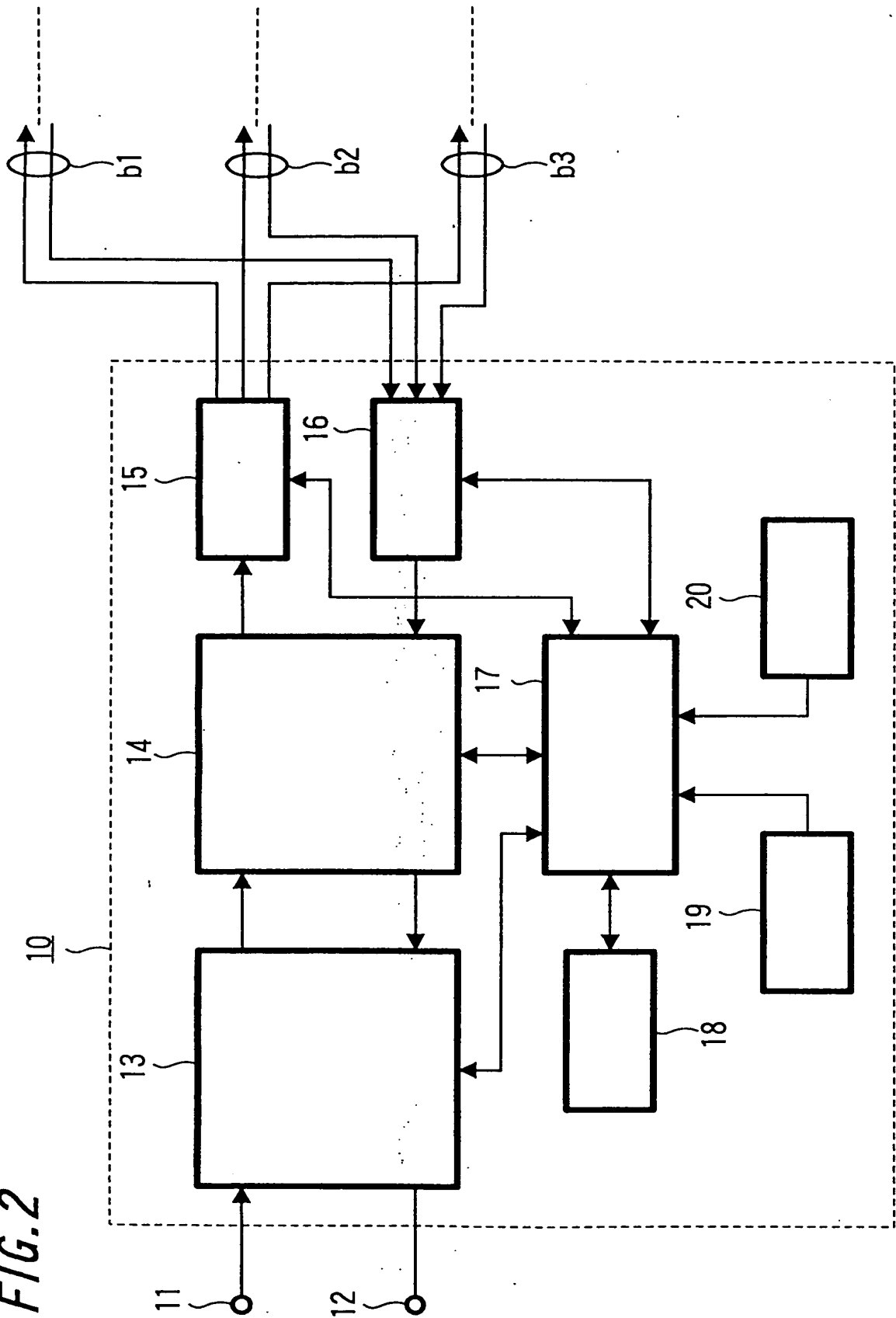


FIG. 3

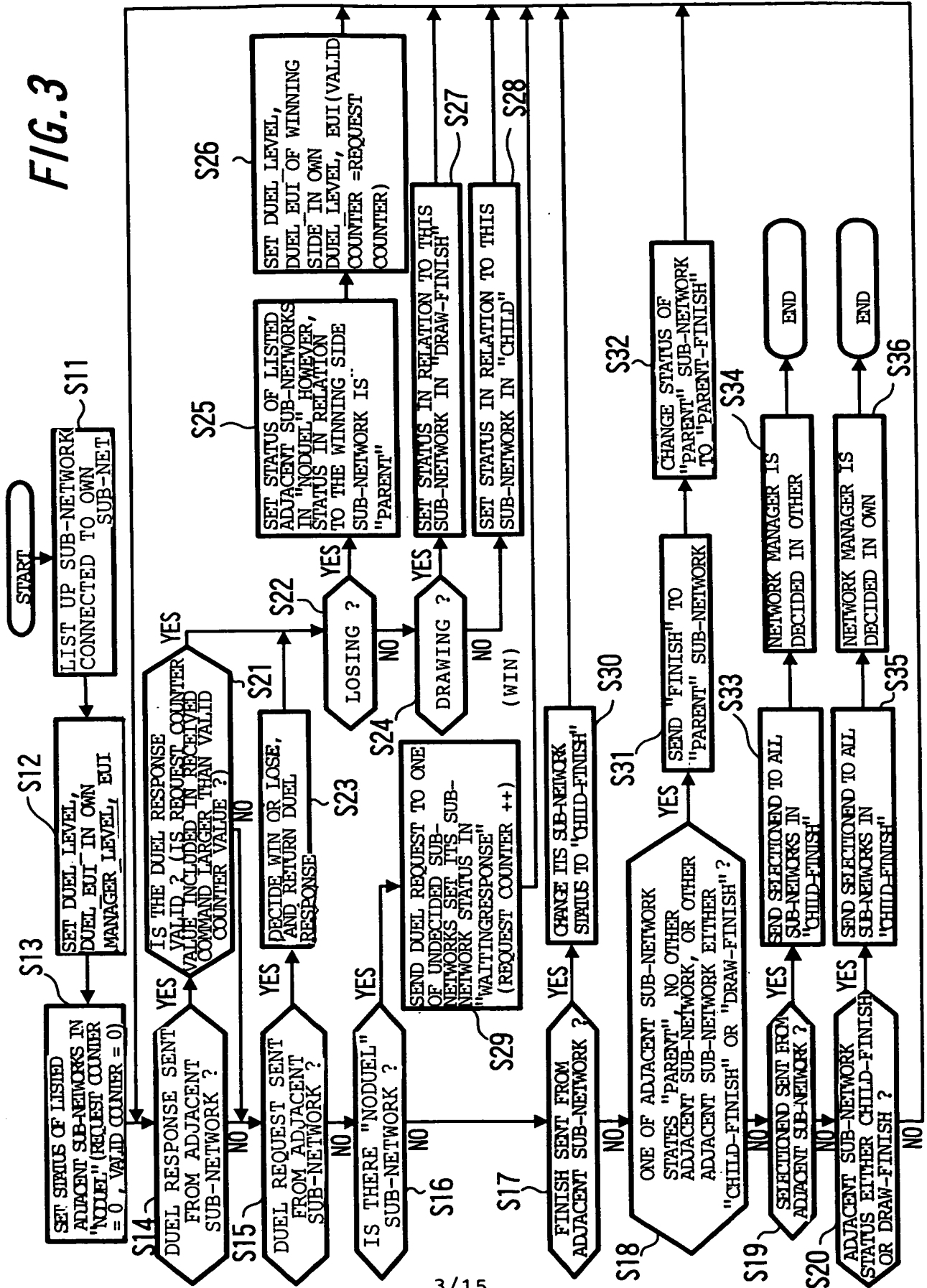


FIG. 4

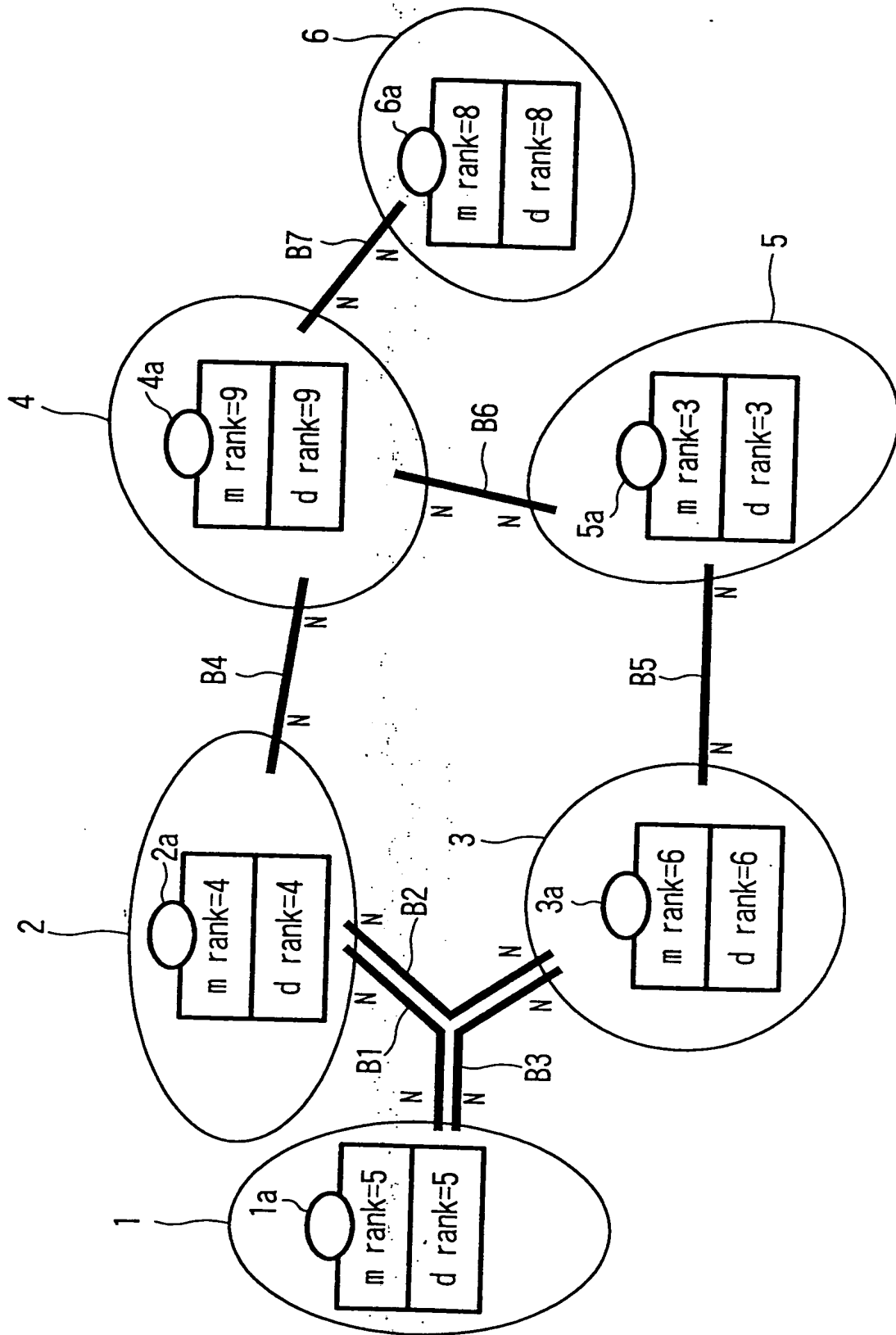


FIG. 5

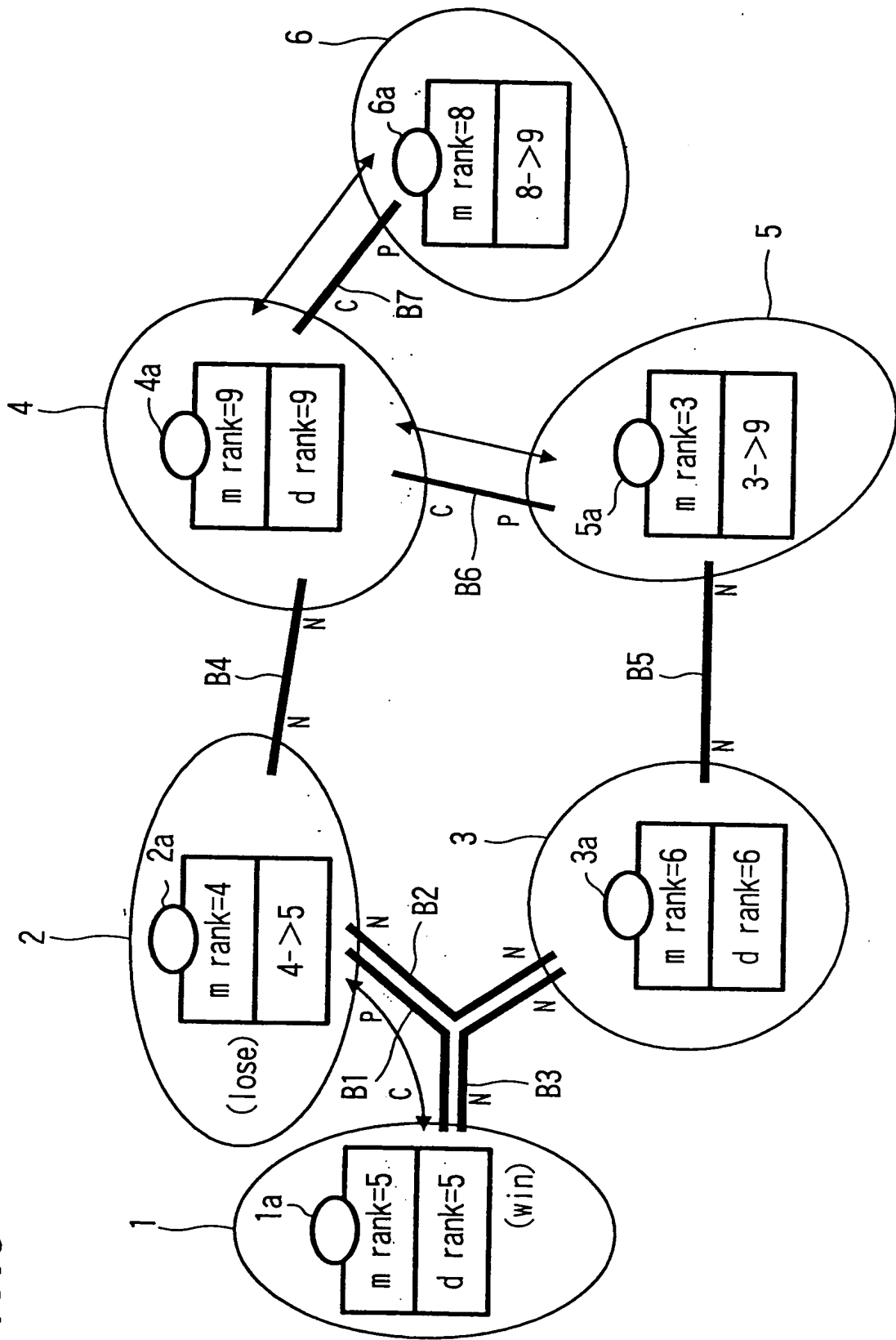


FIG. 6

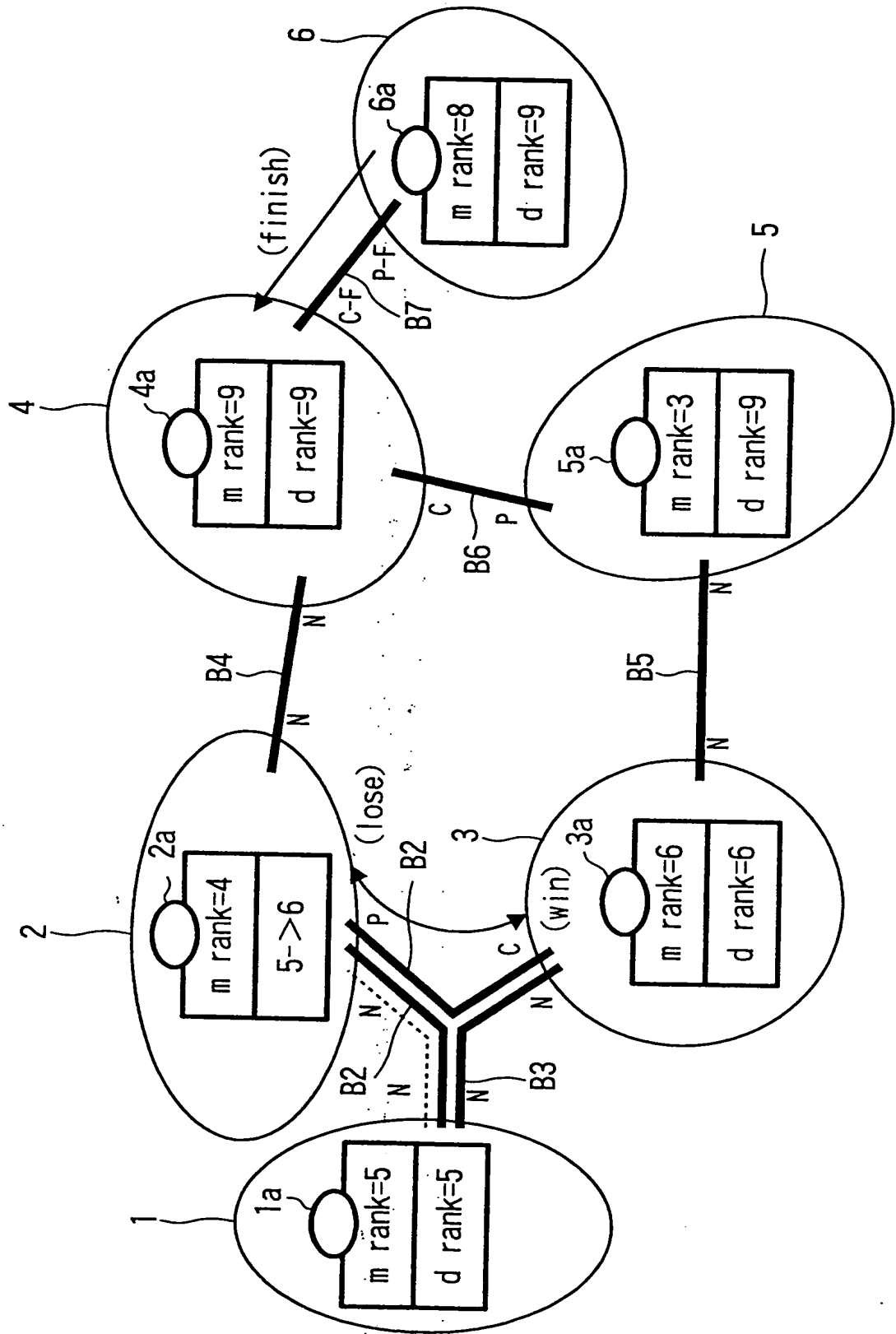


FIG. 7

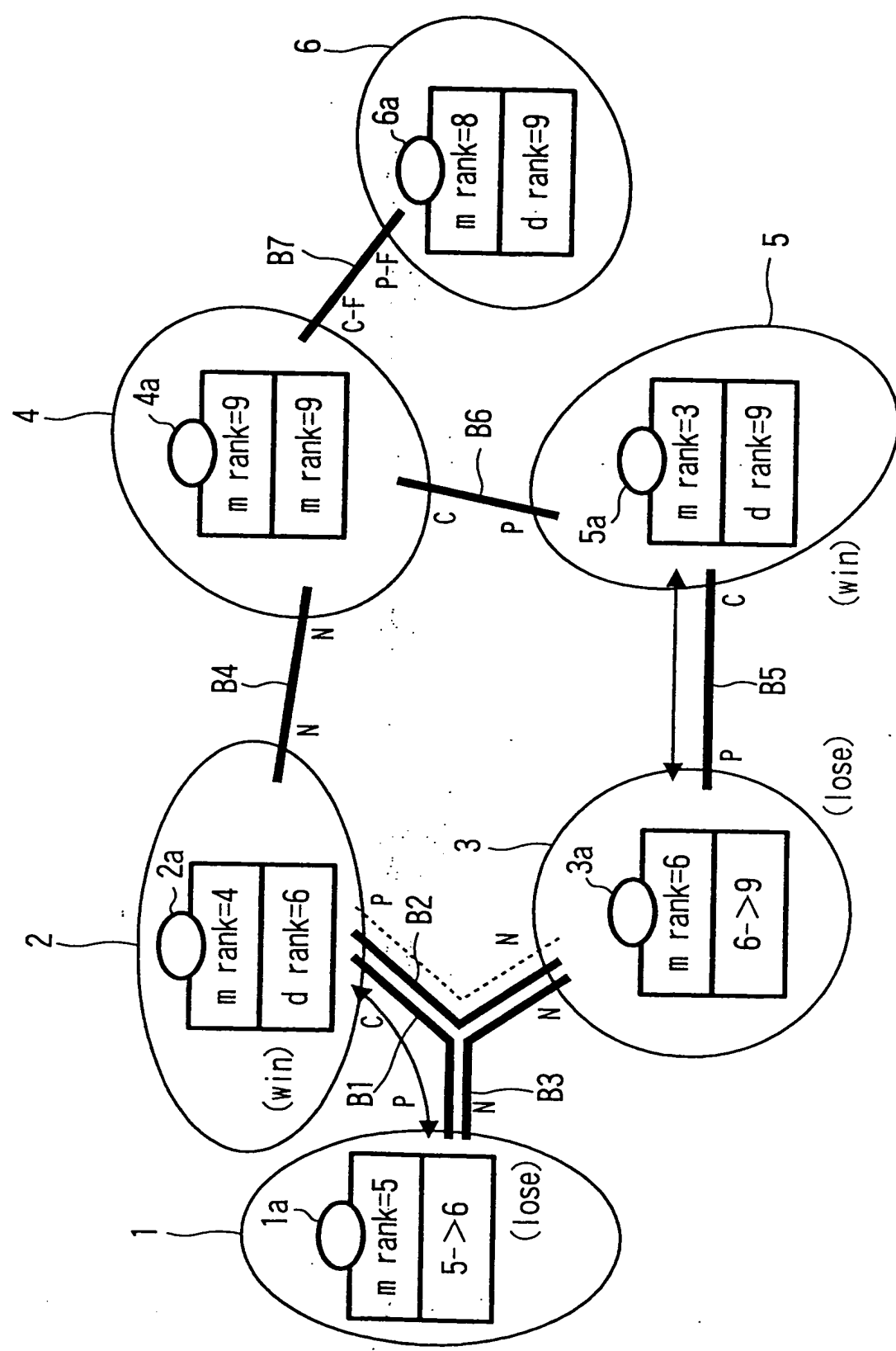


FIG. 8

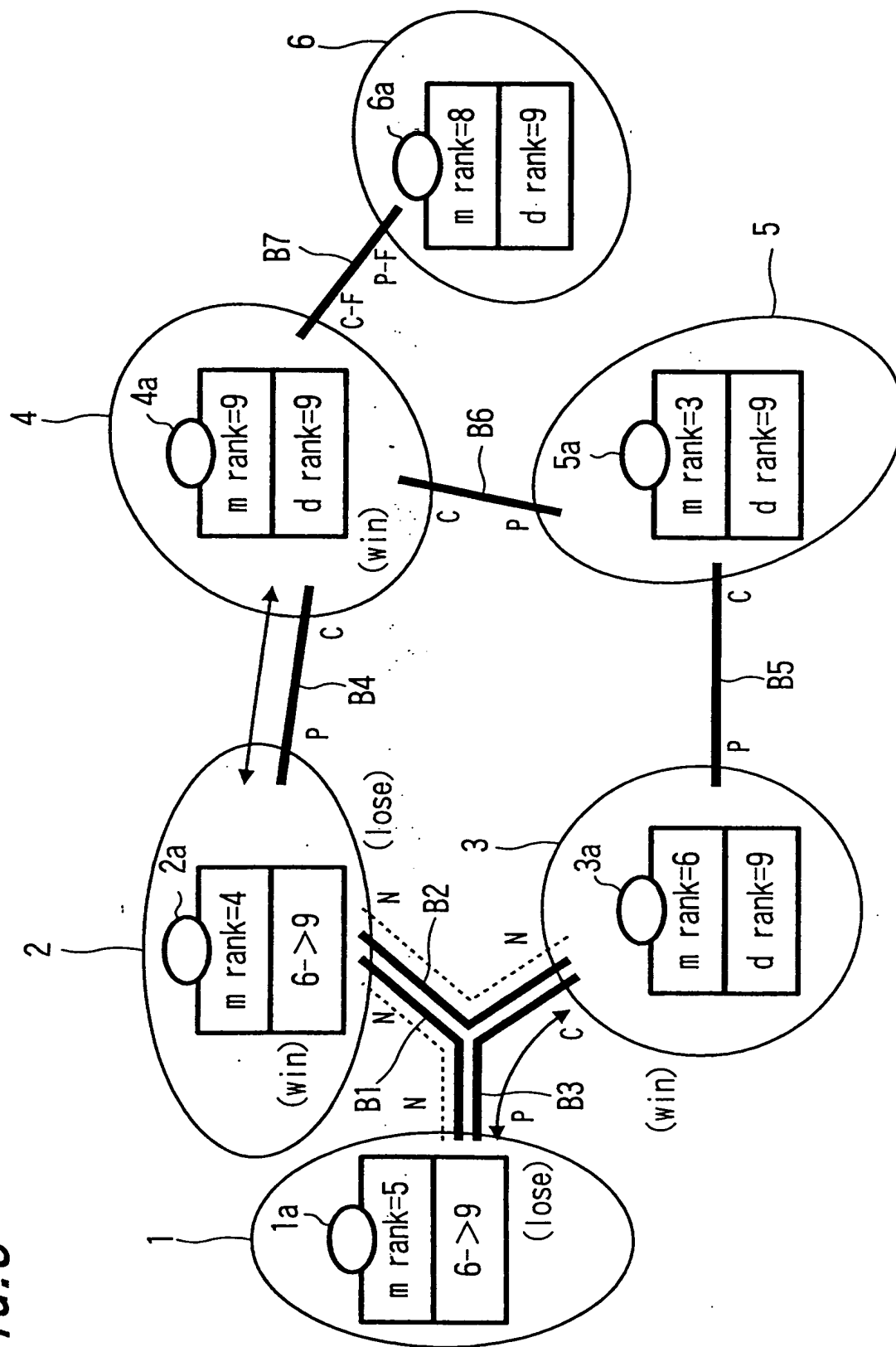


FIG. 9

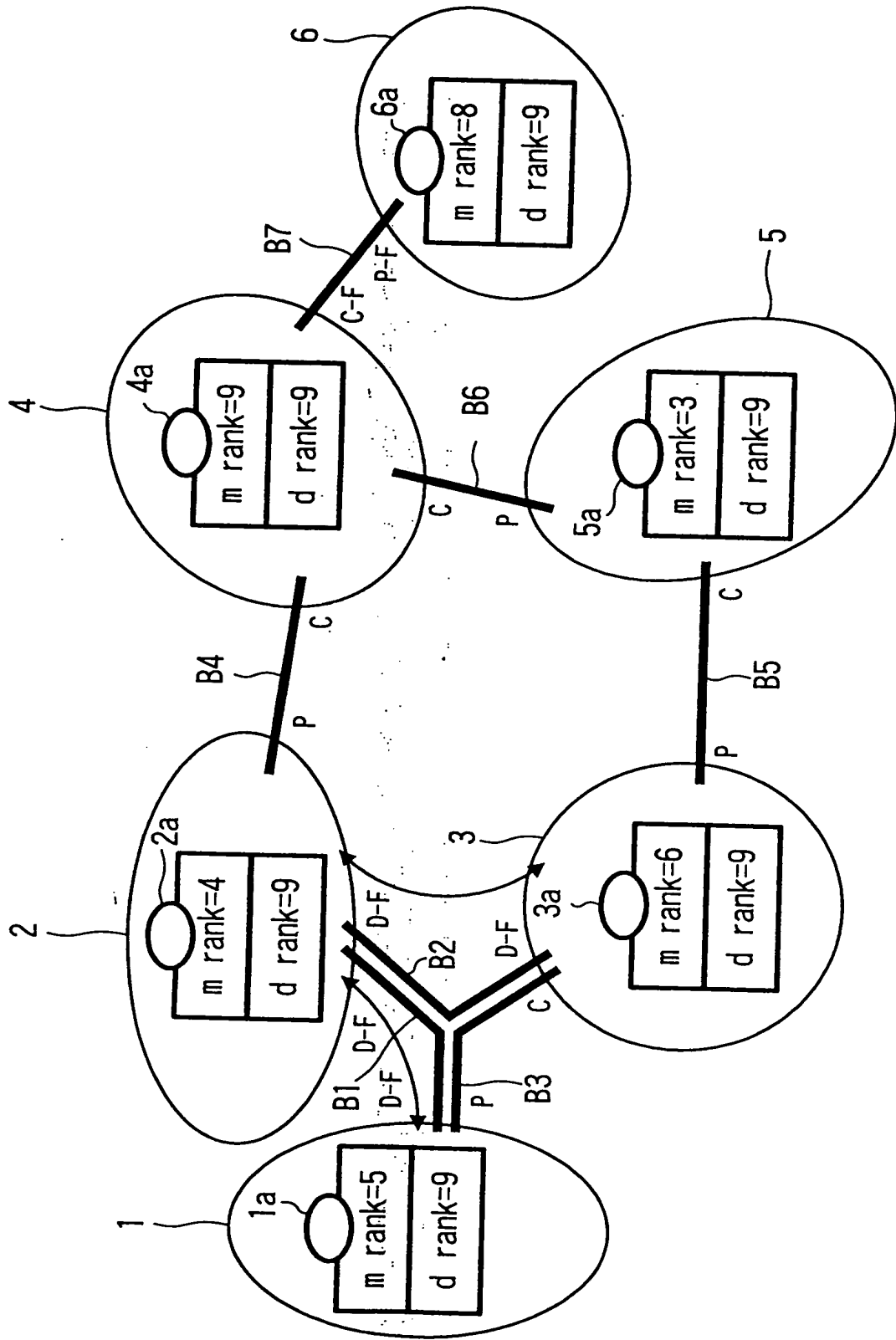


FIG. 10

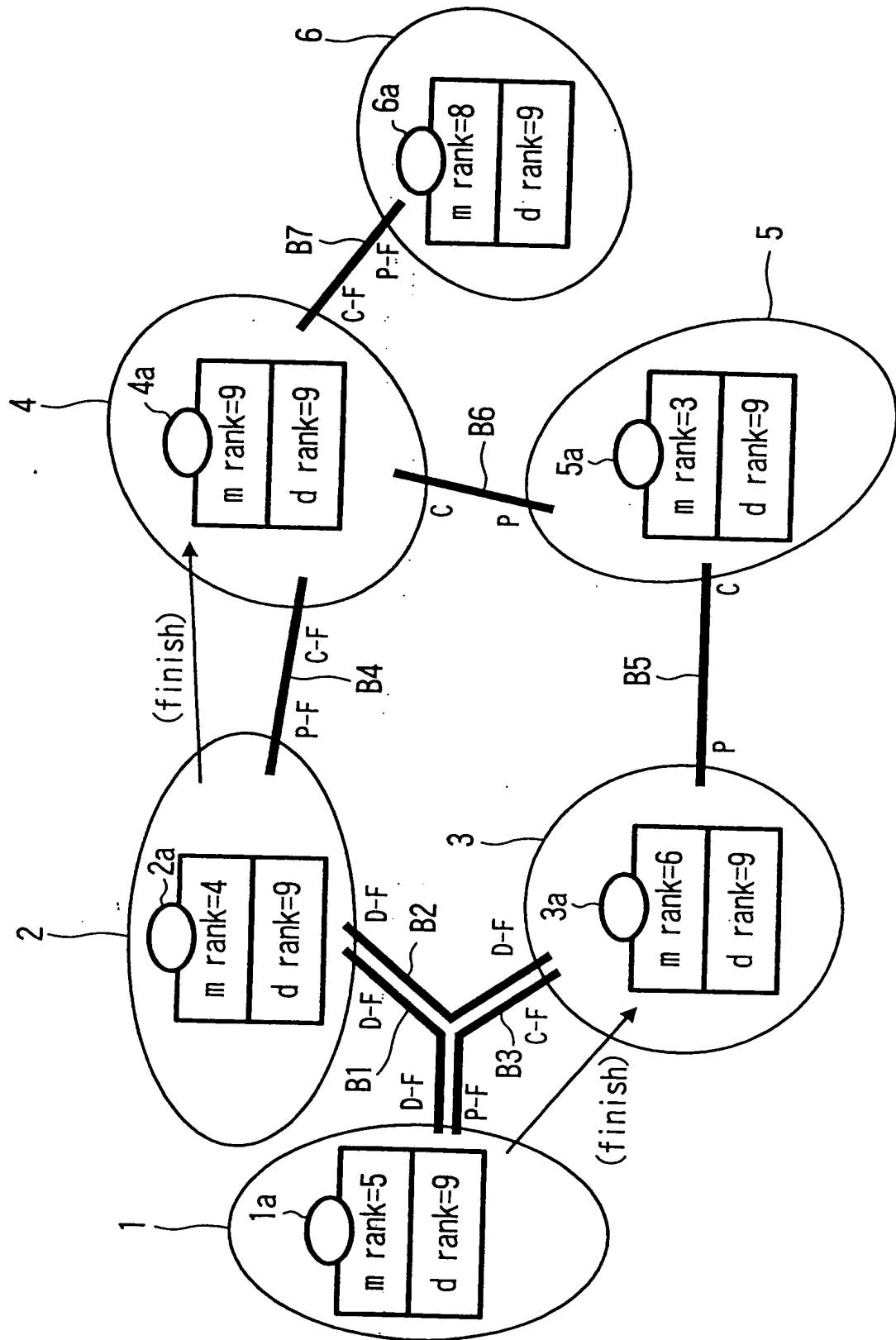


FIG. 11

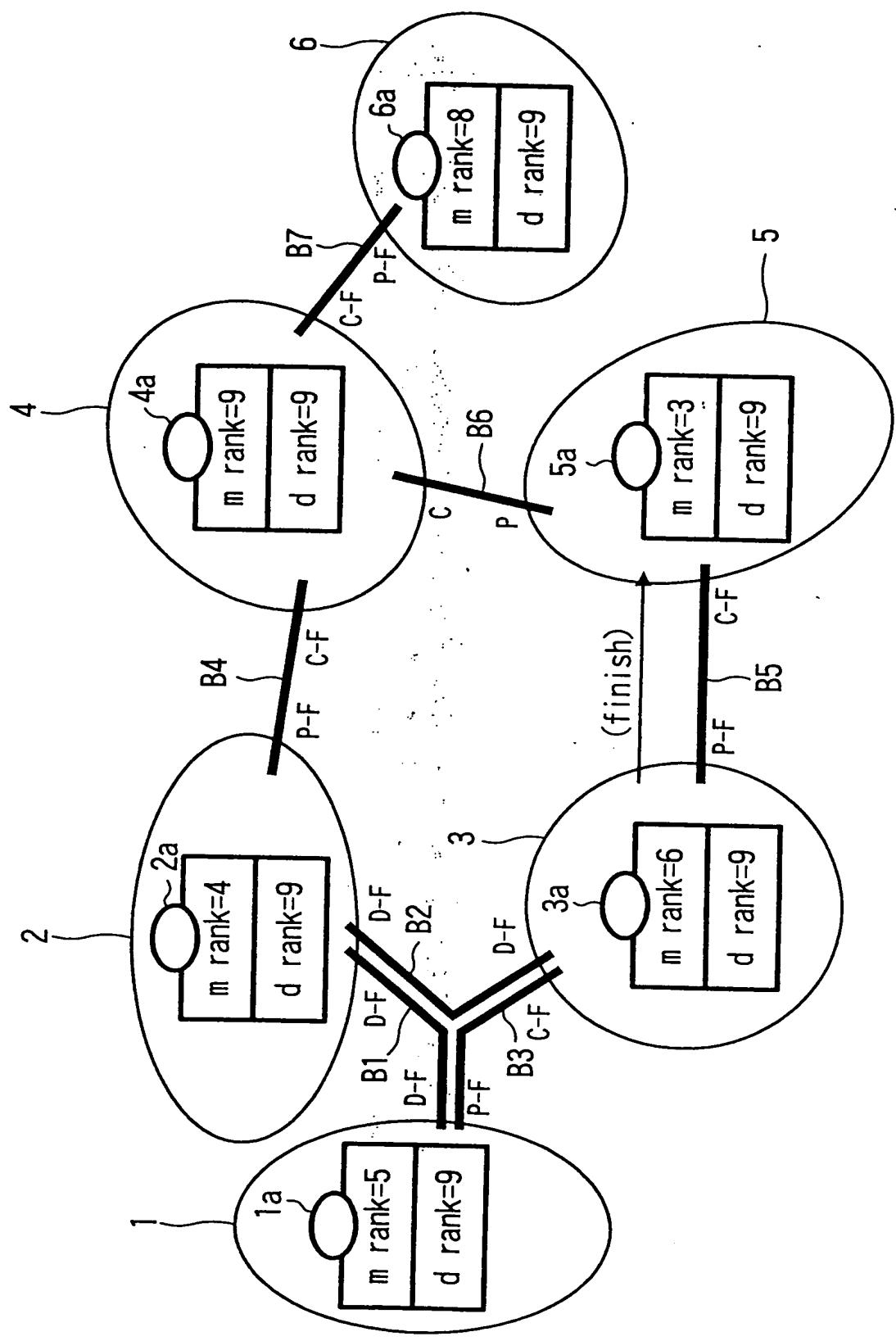


FIG. 12

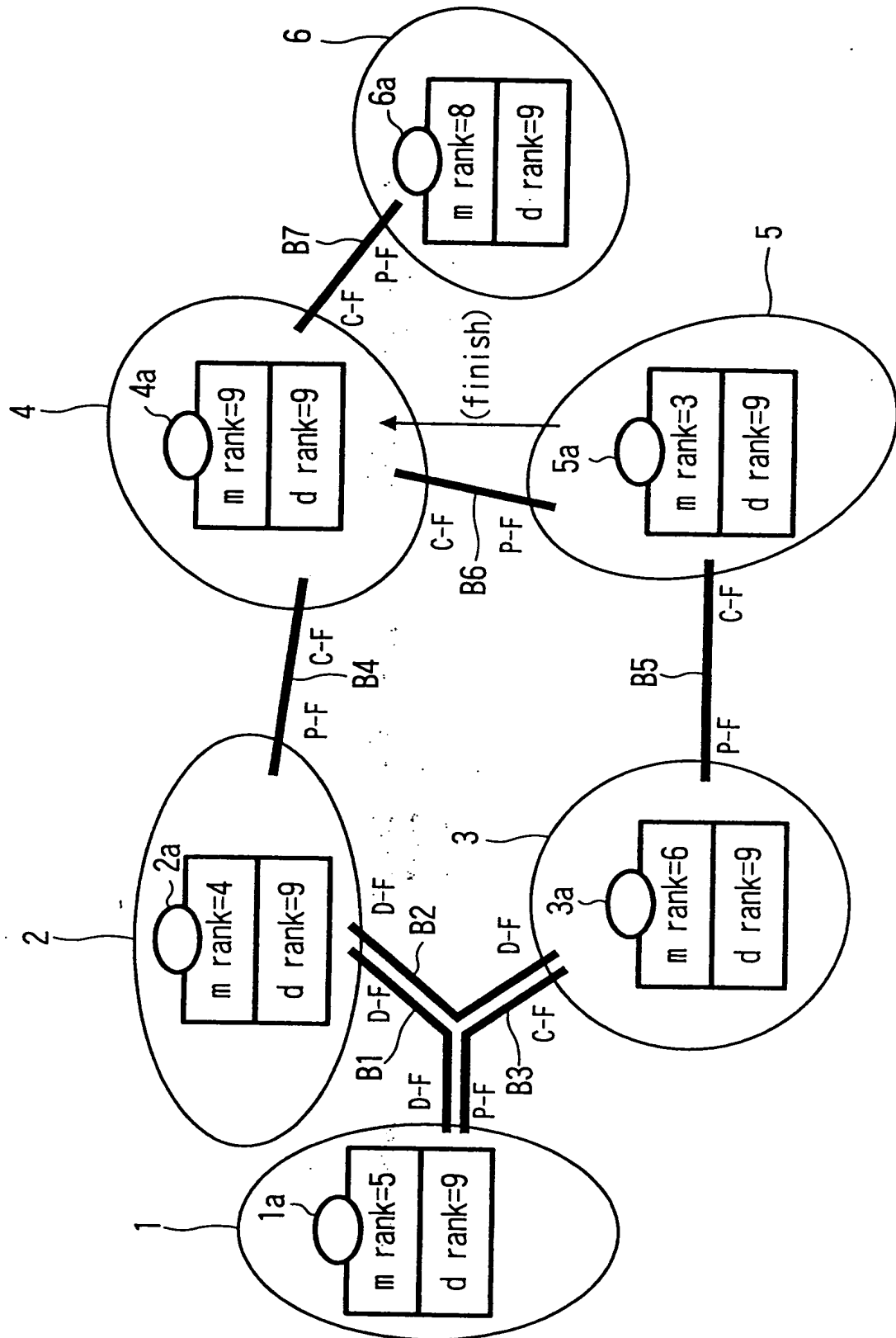
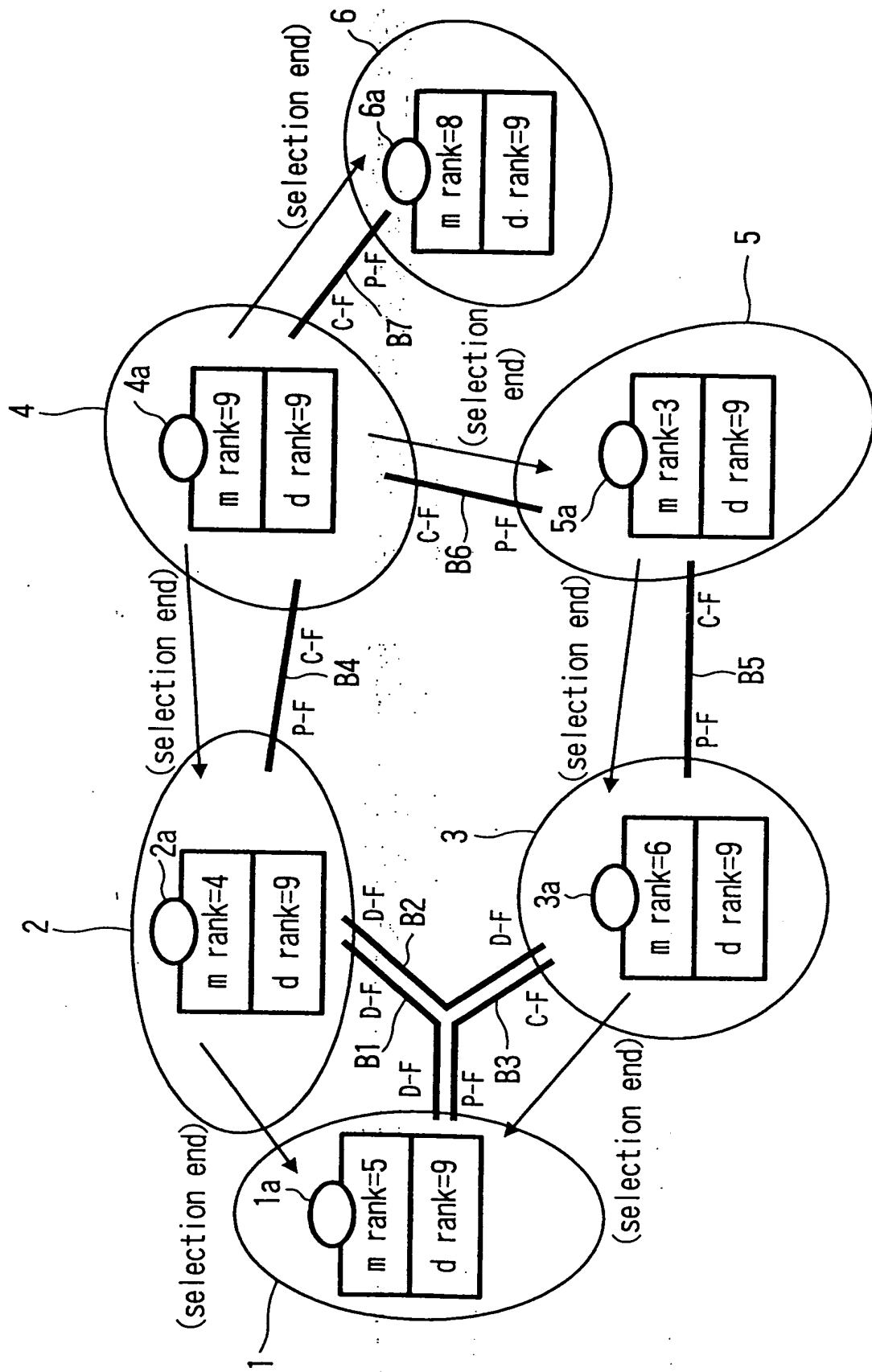


FIG. 13



DESCRIPTION OF REFERENCE NUMERALS

1-6 ... SUB-NETWORK

1a-6a ... SUB-NETWORK MANAGER

10 ... TRANSMISSION DEVICE

11 ... INPUT TERMINAL

12 ... OUTPUT TERMINAL

13 ... DATA INPUT AND OUTPUT INTERFACE

14 ... DATA PROCESSOR

15 ... TRANSMITTER

16 ... RECEIVER

17 ... CONTROLLER

18 ... RAM

19 ... PARAMETER MEMORY UNIT

20 ... ID MEMORY UNIT

100 ... NETWORK SYSTEM

110 ... SUB-NETWORK

111-113 ... BUS

114, 115 ... FIRST BRIDGE

120 ... SUB-NETWORK

121-123 ... BUS

124, 125 ... FIRST BRIDGE

130 ... SUB-NETWORK

131, 132 ... BUS

133 ... FIRST BRIDGE

140 ... SUB-NETWORK

141-143 ... BUS

144, 144 ... FIRST BRIDGE

150 ... SUB-NETWORK

151, 152 ... BUS

153 ... FIRST BRIDGE

161-164 ... SECOND BRIDGE

B1-B7 ... BRIDGE